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Mar 30, 2009 ... Over the past year we have witnessed server **consolidation** occurring at a ... The single-chip CNA handles **storage** and **data** network traffic at full ... The QLogic single-chip, highly integrated ASIC **design** eliminates the ...

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... Managing Shared **Storage** in a Sun Cluster 3.0 Environment With Solaris Volume .... The detailed process of **data** center **design** appears on the outset to be a purely ... When entering the **Pantheon** in Rome, most people probably don't remark, ... Evolution of the **Data** Center: Moving Toward **Consolidation**: By David ...

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Nov 3, 2008 ... The details of each sub-use case will be discussed in the **design** section below. .... How long should the white board store the **data**? ... either as separate lists or as a **consolidated** (union or intersection set?) list. ... The KoiosWB **Storage** Manager: developed as an implementation of the GCP ... **pantheon**.  
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Oct 10, 2008 ... The project will be based on Muse, a de novo **design** product that Tripos plans to release later this year as part of its **Pantheon** computer-aided drug **design** platform. ... IGSP's **data storage** infrastructure has grown from 4 TB to 300 TB ... **storage** to **consolidated**, network-attached NetApp FAS systems. ...

[www.genomeweb.com/.../tripos-astrazeneca-worldfusion-ariadne-boston-university-nonlinear-dynamics-neta](http://www.genomeweb.com/.../tripos-astrazeneca-worldfusion-ariadne-boston-university-nonlinear-dynamics-neta) - [Cached](#) - [Similar](#)

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2. "Hardware Considerations". In: Real-Time Systems Design and ...

Of course, wait states degrade overall systems **performance**, ..... Code and **data storage**. Ferroelectric RAM. 40 ns. 64 Mbytes. Various ..... replacement algorithm (e.g., first-in-first out; **least recently used**), **write** policy ...

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3. [PDF] Automatic Parallelization and Minimization of Communication Costs ...

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During the last decade, high-**performance** computing has seen a tremendous increase in speed and efficiency. While faster processors, memory subsystems, **storage** media and buses have ... indivisible unit, which is characterized by the **data** items it accesses. ... **least recently used**, **write** back, **write** allocate, **write** ...

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**Design** objective for mass **storage systems** include cost, **performance**, ..... wishes to **merge** or **consolidate** data **storage systems** as described above. ....

Read Hit: Percentage of I/Os that reread data from cache Read Miss: .... It is good for the link bandwidth to be at **least** as large as the average **write workload** ...

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## 2. [PDF] Performance Boosting and Workload Isolation in Storage Area ...

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**consolidate** their server and **storage** resources into data .... or **Least Frequently Used (LFU)** replacement algorithms ... If one of them misses the cache (**Hit-Miss**), then the .... array, a **write-intensive workload** is introduced at the 10 minute mark. .... that a balanced **storage system design** with the right ...

[www.soe.ucsc.edu/~ari/Ari-SANCache-MSST06.pdf](http://www.soe.ucsc.edu/~ari/Ari-SANCache-MSST06.pdf) - [Similar](#)

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## 3. Characteristics of I/O traffic in personal computer and server ...

IBM **Systems Journal** issue 42-2, **Storage Systems** - Feature paper. ... and to revalidate rules of thumb that are useful for **systems design** and sizing. .... This finding suggests that read **performance** in personal **system workloads** could ..... cache **miss** ratios assuming a **write-back** cache with the **least-recently-used** ...

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## 4. The design of a lockup-free cache for high-performance ...

accesses always appear to **hit** at the cache since a **write** to a word ..... ber of **miss** buffers **used** on average. It is possible to estimate ... switches, the cache can adapt to such **workload** changes if ... this scheme is probably best implemented in large **systems** with at **least** several dozens of processors. ...

[ieeexplore.ieee.org/iel2/222/1696/00044672.pdf?arnumber=44672](http://ieeexplore.ieee.org/iel2/222/1696/00044672.pdf?arnumber=44672) - [Similar](#)

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## 5. Chapter 18 MEASURING PERFORMANCE

the number of minutes until the first cache **hit/miss** after a breakdown ..... of any cache replacement policy that is based on the **Least Recently Used** (LRU) ... only characterize Web proxy **workload** but also discuss the **design** of a light- ... than a version of Squid that ran on the high **performance** XFS file **system**. ...  
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#### 6. [HashCache: Cache \*\*Storage\*\* for the Next Billion](#)

Cache **storage system design** has been shaped by this class of users, leading to **design** decisions that fa- ... suitable for a range of **workloads** in developing regions. .... disk seek rate, the hit rate, the miss rate, and the **write** ..... alytical models, we expect a **performance** of at least 320 ...  
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memory **system performance** when it reduces the average L1 **miss** latency. The following equation describes ..... been a local **hit** if replication was increased. If so, ASR increments ... counters [32] to estimate which blocks are **least recently used**. .... **performance** to a wider variety of **workloads**. **Storage** Overhead. ...  
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#### 8. [Design of the 64-bit Option for the Oracle7 Relational Database ...](#)

A cache **miss**, in contrast, is the single largest contributor to ... For instance, one or more of the **least** recently **used** buffers may be evicted from ..... a series of **performance** tests for a DSS-class **workload** of the Oracle7 **system**, ... scans and sorts/**merge** into smaller work units, and executes them concurrently. ...  
[www.hpl.hp.com/hpjournal/dti/vol8num4/vol8num4art6.txt](http://www.hpl.hp.com/hpjournal/dti/vol8num4/vol8num4art6.txt) - [Cached](#) - [Similar](#)

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Traditionally, combining compute and **storage workloads** was extremely complex ..... DRAM provides a local buffer to accelerate Flash **write performance** and maintain ..... File **system** DNLC Entries. Raw. DNLC Accesses. Hit/Miss, Raw .... All of the Sun servers **used** in Sun **Storage** 7000 Unified **Storage** Systems include an ...  
[www.dewpoint.com/.../SunStorage7000Architecture\\_WhitePaper\\_31Oct08.pdf](http://www.dewpoint.com/.../SunStorage7000Architecture_WhitePaper_31Oct08.pdf) - [Similar](#)

#### 10. [Geeking with Greg: 08/01/2008 - 09/01/2008](#)

A near the end of the talk, Jitendra contrasted the techniques **used** for face ... **systems** that makes them better suited to data-warehousing **workloads**", ... that summarizes a Google talk at SIGMOD 2008 on a **storage system** they .... It returns the stored top-k results if a query is a **hit** or reports a **miss** otherwise. ...  
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1. [Intelligent \*\*Storage\*\*: Cross-Layer \*\*Optimization\*\* for Soft Real-Time ...](#)

In current narrow **interface design**, the ability to pass valuable infor- ... human-centered management and **configuration** overheads. Advancements in .... To perform **workload-aware system optimization**, it is mandatory that the in- ... ple, the request **merge** and separation of device driver bottom-half. When the ...  
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A case at hand is the Virtual **Interface** Architecture (VIA), a **user-level** memory-mapped .... and **storage** performance at the lowest possible **system configuration** cost. ... Recent research on network attached **storage systems** suggests the use of .... To evaluate ARM FSC **workload** for the proposed SD architecture, ...  
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**Design** objective for mass **storage systems** include cost, performance, ... of disk array data **storage systems** concerns **optimizing** the **storage** capacity while .... An exemplary **interface user** screen 406 for the tool is further shown in FIG. ... 21 shows the **user configuration** selected after the **user** clicks "calculate. ...  
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4. [\[PDF\] BORG: Block-reORGanization for Self-\*\*optimizing Storage Systems\*\*](#)

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evaluation of BORG, a self-**optimizing storage system** .... **user** and the SVN server **workloads**. Further, for each of .... In this section, we present the **design** details of BORG .... the latter two phases, the I/O scheduler **merge** ratio and .... 14% and 18% for cumulative and partial

**configuration.** It ...

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## 5. Borg: Self-Optimizing Block Storage.

Jan 13, 2009 ... BORG: Block-reORGanization for Self-optimizing Storage Systems .... The office and developer **workloads** are single-user **workloads**. .... The complexity of the **merge** process increases if two vertices (either within the same ... the actual data movement to realize the new **configuration** of the BOPT. ...

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## 6. Physical Design Refinement: The "Merge-Reduce" Approach

meet the constraints on the physical **design** (such as **storage**, update cost, or ... designs for indexes and materialized views in a database **system**. .... **re-optimize** the inferred **workload** under the new **configuration** CF. We ...

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## 7. Index tuner for given workload - Patent 6266658

The **system** of claim 25 and further comprising a **merge** module that merges indexes .... An object may support an **interface** by providing executable code for the .... **configuration** R' is then evaluated for the **workload** by **optimizing** each query in ... The **configuration** may be used by a **user** or **system** administrator to ...

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## 8. [ps] Self-Tuning Technology in Microsoft SQL Server

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the self-tuning advances in the query engine and the **storage** engine respectively ... load of the **system**. An important component of physical database **design** is selecting ... Microsoft. SQL Server. **Optimize Query. Simulate Configuration. Workload** ... ample, if a **merge** join obtains its data from two sort operations, ...

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## 9. Fault Recovery Designs for Processor-Embedded Distributed Storage ...

the high-level architectural **design** of the smart **storage system**. A disk-based smart node (SN) ... the **system** parameters are used for code **optimization**. The ... smart **storage system** for various DB and non-DB **workloads**. Aggregate. IndexScan ... **User interface**. Cost models.

**System catalogs.** (for database). **SD system ...**

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10. [poc] SciDAC Preproposal - **Systems Software Center**

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The **user interface** will be modeled on existing queue **systems** such as PBS and will .... The ideal scalable **systems design** environment would include a suite of simulator ... (60 months) Administrative tools to automate **workload configuration** .... Linux and Windows clusters, the mass **storage system** and networking. ...

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S Palacharla, RE Kessler - Computer Architecture, 1994., Proceedings the 21st ..., 1994 - [ieeexplore.ieee.org](#)

... codes show much lower primary cache miss rates than ... to any particular memory system design details. ... Table 1: Benchmark Characteristics This table describes the ...

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RH Saavedra, AJ Smith, University of ... - IEEE Transactions on Computers, 1995 - CiteSeer

... As part of this work, we describe the design and perfor- ... The SPEC (Fortran) benchmark work ... cache miss penalty, although the measurement was made at the machine ...

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D Callahan, A Porterfield - Proceedings of the 1995 ACM/IEEE conference on ..., 1990 - [portal.acm.org](#)

... The benchmark programs used for this work in- cluded ... LINPACKD - the standard LINPACK bench- mark [Don81] ... for the program is affected by the cache structure, the ...

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A Agarwal, J Hennessy, M Horowitz - ACM Transactions on Computer Systems (TOCS), 1988 - [portal.acm.org](#)

... Thus a viable design alternative is to overcome ... II in the Appendix summarizes benchmark trace characteristics ... We present cache miss rate results throughout this ...

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[\[PDF\] The declining effectiveness of dynamic caching for general-purpose microprocessors](#)

DC Burger, JR Goodman, A Kagi - 1995 - [eprints.klupm.edu.sa](#)

... designers are increasingly being forced to design machines with ... of the addresses produced by the benchmark code from ... opt model and a normal (L1) cache, using a ...

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AJ Smith, RH Saavedra, AJ Smith - IEEE Transactions on Computers, 1995 - [eprints.klupm.edu.sa](#)

... We show that the SPEC benchmark results on these machines ... 3.2.5. Write Buffers A cache, in which the update ... in the write buffer, it can in some designs (eg the ...

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RH Patterson, GA Gibson, E Gitting, D ... - Proceedings of the fifteenth ACM symposium on ..., 1995 - [portal.acm.org](#)

... Prefetching strategies are needed to "parallelize" these workloads. ... management based on informed prefetching and caching. ... Benchmark applications and ...

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[PDF] •Systematic computer architecture prototyping

TM Cortie - 1992 - Citeseer

... Evolution of energy for gcc benchmark during execution ... of the overall design process ...  
emphasis on cache-based memory systems and superscalar processor components of work ...

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Improving the efficiency of UNIX buffer caches- \*kloppm.edu.sa (PDF)

A Braunstein, M Riley, J Wilkes - ACM SIGOPS Operating Systems Review, 1989 - portal.acm.org

... The primary XMF design goal was to minimize the READ ... operations, and to collect hardware cache and TLB ... to be highly repeatable: the benchmark programs were ...

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Wisconsin architectural research tool set- \*wisc.edu (TXT)

MD Hill, JR Larus, AR Lebeck, M Talluri, DA ... - ACM SIGARCH Computer Architecture News, 1993 - portal.acm.org

... Smith, "Cache Performance of the SPEC Benchmark Suite," to ... scheme, and adaptation  
of this design to a ... by the least recently used (LRU) cache replacement policy ...

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**Optimizing instruction cache performance for operating systemintensive workloads** - [tsau.edu \(PDF\)](#)

J Torrellas, C Xia, R Daigle - First IEEE Symposium on High-Performance Computer ... , 1995 - [ieeexplore.ieee.org](#)

... affected by the degree of placement **optimization** for the ... To gain insight into the **cache performance** of operating sys ... use this insight later to **design** the code ...

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**Cache-DASD storage design for improving system performance**

CP Grossman - IBM Systems Journal, 1965 - [research.ibm.com](#)

... Through an **iterative** process, a set ... RPQ, illustrates a different ap- proach. I2 In this **design**, the **performance** ... to one of these slots and **read** into **cache** storage ...

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A Agarwal, J Hennessey, M Horowitz - ACM Transactions on Computer Systems (TOCS), 1998 - [portal.acm.org](#)

... Thus a viable **design** alternative is to overcome ... and Definitions The **cache** and **workload** parameters pertinent ... We present **cache miss** rate results throughout this ...

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**Approaching a machine-application bound in delivered performance onscientific code**

WH Mangione-Smith, TP Shih, SG Abraham, ... - Proceedings of the IEEE, 1993 - [ieeexplore.ieee.org](#)

... bottlenecks that are critical to the **design** of today ... effort is spent in program **optimization**, and enough ... dispatch bandwidth of the Instruction-Cache Unit (ICU). ...

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RH Patterson, GA Gibson, E Gitting, D ... - Proceedings of the fifteenth ACM symposium on ... , 1995 - [portal.acm.org](#)

... Prefetching strategies are needed to "parallelize" these **workloads**. ... just as fast as the **caches** [Ousterhout85, Balcal91 ... are needed to boost I/O **performance**. ...

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**(PDF) The declining effectiveness of dynamic caching for general-purpose microprocessors**

DC Burger, JR Goodman, A Kagi - 1995 - [eprints.kupm.edu.sa](#)

... designers are increasingly being forced to **design** machines with ... was run with the default input for 20 **iterations**. ... n-opt model and a normal (L1) **cache**, using a ...

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[PDF] • **Measuring cache and TLB performance** and their effect on benchmark runtimes

RH Saavedra, AJ Smith, University of ... • IEEE Transactions on Computers, 1995 • CiteSeer

... of this work, we describe the **design** and perfor- ... improvement due to **optimization**

as an improvement in ... **cache miss** penalty, although the measurement was made at ...

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RA Uhlig, TN Mudge - ACM Computing Surveys, 1997 • [portal.acm.org](#)

... bus of a host computer while it runs a **workload**. ... to collect address traces for TLB

and **cache simulation**. ... of 32 bits each) necessitated the **design** of circuitry ...

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### Cache-DASD storage design for improving system performance

CP Grossman - IBM Systems Journal, 1985 - research.ibm.com

... To take this step, the following key **design** objectives had to ... The **cache** management algorithms were designed to provide the ... Through an **iterative** process, a set ...

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### Informed prefetching and caching - cs.cmu.edu [PDF]

RM Patterson, GA Gibson, E Ginting, D ... - Proceedings of the fifteenth ACM symposium on ... 1995 - portal.acm.org

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### Improving the efficiency of UNIX buffer caches - cs.kupm.edu.sa [PDF]

A Braunstein, M Riley, J Wilkes - ACM SIGOPS Operating Systems Review, 1989 - portal.acm.org

... The primary XMF **design** goal was to minimize the ... obtained from measuring "micro-level" **performance** parameters. ... from the file buffer **cache** **READ** results were ...

Cited by 15 - Related articles - All 11 versions

### [PDF] Systematic computer architecture prototyping

TM Cortie - 1992 - Citeseer

... of convergence of the overall **design** process ... the number of **iterations** and enlarging ... emphasison **cache**-based memory systems and superscalar processor components software ...

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### Evaluation of closely coupled systems for high performance database processing

E Rahm - Distributed Computing Systems, 1993, Proceedings the ..., 1993 - ieeexplore.ieee.org

... GEM and magnetic disks with or without disk **cache**. ... important **workload** type for evaluating the **performance** of transaction and ... **workload** generation and allocation ...



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[PDF] \*Itanium processor microarchitecture

H Sharangpani, K Arora - IEEE Micro, 2000 - membres.lycos.fr

... **design** employs robust, scalable circuit **design** techniques ... Dynamic hardware for runtime

**optimization** While the processor ... is present in the target address **cache**. ...

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[PDF] \*Hardware techniques to improve the performance of the processor/memory interface

DC Burger - 1998 - cs.utexas.edu

... issues in computer system **design**. ... We show that the indirect **Cache** works

synergistically with the traffic **optimization** techniques described in ...

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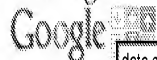
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### 2. Method and system for **capacity planning** and configuring one or ...

Jun 24, 2008 ... A method for determining a **configuration** for a **data storage** .... The **simulation** system or tool preferably uses software 342 for .... for **design** purposes or for **consolidation** or **capacity planning** or general performance analysis. .... In addition to the **workload** or use requirements, total **capacity** ...  
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 by D Aharoni - 2008

### 3. Course 9050: **Capacity Planning** Boot Camp - TONEX.com

**capacity planning** seminar, **capacity planning** course. ... **capacity planning** project; Explore benchmarking, load testing, **workload** forecasting ... **planning**; Emphasis is on practical methods of measurement, **simulation**, and analytical modeling ... monitoring **data** accumulation; Detection, counting, **storage** and reporting ...  
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### 4. [PDF] **Capacity Planning** Best Practices

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**consolidation** and **capacity planning**. **Data** requirements for **capacity** management differ ... change the **workload** or **configuration** and to try to improve performance prior to ... **consolidation** or **workload optimization**. Integrated **simulation** modeling using .... traffic, minimize **data storage** requirements at the user end, ...  
[www.sightlinesystems.com/pdf/CapacityPlanningBestPractices.pdf](http://www.sightlinesystems.com/pdf/CapacityPlanningBestPractices.pdf) - [Similar](#)

### 5. **Capacity**: Desktop, Device and Server Management

Requirement: Automated Infrastructure **Capacity Planning** for Primary IT Services .... use of **simulation** software make it possible to investigate **capacity planning** ... Analysis of the monitored **data** may identify areas of the **configuration** .... **design** tasks fit in the overall scope and sequence of the **consolidation**, ...

[technet.microsoft.com/en-us/library/bb621280.aspx](http://technet.microsoft.com/en-us/library/bb621280.aspx) - [Cached](#) - [Similar](#)

## 6. Performance & Capacity Planning for SANs

Oct 27, 2008 ... **configuration** and **capacity planning** for solaris servers ... performance by **design** computer **capacity planning** .... **Lans** • **Consolidation** of servers, **storage**, **SANs** Availability • **Data** Availability Network .... being Read From "Server\_A" Traffic Analysis **Capacity Planning** **Workload** Analysis Diagnostics ...

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## 7. PDF Business-aware Capacity Planning

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two main product lines: **Capacity Planning** (since 2000) and ... Safe **consolidation**: placement **simulation** and **optimization** enable ... safe **data-center consolidation** and virtualization initiatives ... sizing new systems (Service **Design**). Continuous ... (e.g. according to compatible **workloads**) with respect to ... [www.fondazionepolitecnico.it/.../Fabio%20Vicente\\_Neptun.pdf](http://www.fondazionepolitecnico.it/.../Fabio%20Vicente_Neptun.pdf) - [Similar](#)

## 8. SYSTOR 2009 - The Israeli Experimental Systems Conference

We successfully integrated STAMP into a power-aware **capacity planning** tool to ... to enabling efficient **data center design** and true I/O **consolidation**. .... on write amplification analytically and by **simulation** assuming **workloads** of .... migration and network **configuration**, which allows better **optimization** for ...

[www.haifa.ibm.com/conferences/systor2009/abstracts.shtml](http://www.haifa.ibm.com/conferences/systor2009/abstracts.shtml) - [Cached](#) - [Similar](#)

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Caplan™ is the business-aware **Capacity Planning** solution for large **data centers** ... performance and **workload** metrics, **configuration data**, operational events as well as ... resource performance with **workloads** (e.g. **storage** size and CRM users) ... Safe **consolidation**: placement **simulation** and **optimization** enable safe ...

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## 10. Server Consolidation Using Performance Modeling

At what **workload** does the user response time fail to meet performance requirements? ... that had service-level agreements in place and processes for **capacity planning**; ... **Data** collection **planning**. Here, we determined metrics and sources of ... in a performance model, using a popular **simulation** modeling product. ...

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3. The Art of **Capacity Planning** by O'Reilly Media

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## 4. Performance Evaluation : A page fault equation for modeling the ...

There are also many papers on the **design** of replacement algorithms [16], [21] and [22] that do not .... Intuitively, although the **simulation data** (Fig. 2, Fig. 3 and Fig. .... Specifically energy conservation, **capacity planning**, **workload** .... and extrapolation, and for **optimization** of the cache **configuration**. ...  
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 by YC Tay - 2006 - [Cited by 3](#) - [Related articles](#) - [All 3 versions](#)

5. Database access characterization for buffer **hit** prediction - **Data** ...

Note that in the **simulation**, apage is never replicated in two .... view of system **configuration** and **capacity planning** when .... **workload** where **data** from more than one file are buffered ... agement and Query **Optimization** in Relational Database .... **miss** ratio for a. 2-level **storage** model with LRU or FIFO re- ...

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A review of the various **data** gathering tools used for **capacity planning** ..... **Workload**. **Configuration**. Inter-related. Transactions. Amount of **Data**. Number of users ..... **hit miss**. 8 cycles. Arb/Add. Memory. 2 for UP. **Data** Xfr. 3 cycles ..... mean that you can easily **add storage** on demand. Capitalizing on IBM ...

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#### 8. [Characterization of database access pattern for analytic...](#)

**workload** management and system **capacity planning** ..... are used in the trace driven **simulation** to obtain the buffer **hit** probability vs .... is extremely useful from the point of view of system **configuration** and **capacity** .... Van den Berg, J. and Towsley, D. Properties of the **miss** ratio for a 2-level **storage** ...

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General Terms Algorithms, **Design**, Experimentation, Lan- ... viewed abstractly as an **optimization** problem: a program- .... **workload** is the **data** size of the objects being cached, which .... the cache **hit**. The **simulator** records the net decrease in end- .... 2002; 2005], recovery **planning** in **storage** systems [Keeton ...

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**data storage**. Attached to each L2 module is a memory controller. (MC) which directly interfaces to one .... In order to lower **miss** latency and best utilize the L2 **capacity**, .... This section describes the **workloads**, **simulation** platform, and .... the P8 **configuration**, the L2 **hit** stall time includes both L2 hits as ...

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Documented production installation **configuration** instructions and settings. .... The process translates both current and future demands to **workloads**. ..... is now underway with Guerrilla **Capacity Planning: Hit** and Run Tactics for Sizing UNIX, ... \$55 Performance by **Design: Computer Capacity Planning** by Example ...

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Generic online **optimization** of multiple **configuration** parameters with .... A regression-based analytic model for **capacity planning** of multi-tier applications. .... Adaptive algorithms for managing a distributed **data processing workload**. .... Appia: automatic **storage** area network **design**. In Conference on File and ...  
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total DoD **workload** is categorized into ten computational ..... **capacity planning** requires an on-going effort at our Center. ... must try to predict **storage** requirements that are based .... about a three percent **hit** on memory usage for the Altix. ... depending on the system **configuration** and usage patterns. ...  
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**new configuration** above, for example, **workload** X's bottleneck might shift ..... Success stories in **capacity planning**: Mathematical models have tra- ... to know the consequences of migrating **data** from **storage**-node A to **storage**-. .... the **data** path. The request may **hit** in the NFS server cache or **miss** and have ...  
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There are also many papers on the **design** of replacement algorithms [16], [21] and [22] that do not ..... Intuitively, although the **simulation data** (Fig. 2, Fig. 3 and Fig. .... Specifically energy conservation, **capacity planning**, **workload** .... and extrapolation, and for **optimization** of the cache **configuration**. ...

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4. Page 1 Performance Analysis of **Storage** Systems Elizabeth Shriver ...

The cache **hit** and **miss** ratios are approximated in [1,15] using metrics of .... **configuration** tools to support the **design** of disk arrays to meet specific customer ... **Add** additional **storage**, replace current devices with faster ones, or upgrade .... **Workload** characterization. **Simulation** and benchmarking would be more ...

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5. [\[PDF\] Short Paper: Improving the Responsiveness of Internet Services ...](#)

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viewed abstractly as an **optimization** problem: a program- ..... the cache **hit**. The **simulator** records the net decrease in end- ... ately scaled-down **workload** and cache **capacity**. In our im- .... 2002; 2005], recovery **planning** in **storage** systems [Keeton ... as algorithms for efficiently exploring the **configuration** ... [www.cse.ucsd.edu/~arasmuss/papers/fluxo.pdf](#) - [Similar](#)  
by A Rasmussen

6. [Database access characterization for buffer \*\*hit\*\* prediction - Data ...](#)

'Note that in the **simulation**, apage is never replicated in two .... view of system **configuration** and **capacity** **planning** when .... **workload** where **data** from more than one file are buffered ... agement and Query **Optimization** in Relational Database .... **miss** ratio for a 2-level **storage** model with LRU or FIFO re- ... [ieeexplore.ieee.org/iel2/007/0018/00344069.pdf?arnumber...](#) - [Similar](#)  
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7. [\[PDF\] Polus : Growing \*\*Storage\*\* QoS Management Beyond a "Four-year Old Kid"](#)

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**Capacity** **planning**, application/**storage** performance management, backup/restore operations, ... **Design** of high level QoS goals to low level **storage** ac- .... **Workload** trace. Client. Figure 3. **Simulator** of a SAN file-system .... MMDM Metadata **hit** and **data** **miss** in client. MMDM Metadata and **data** **miss** in the client ... [osl.cs.uiuc.edu/docs/fast04/paper.pdf](#) - [Similar](#)  
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8. [The Art of \*\*Capacity\*\* \*\*Planning\*\* by O'Reilly Media](#)

May 15, 2009 ... After the initial **configuration**. 24 organizing (see **capacity** **planning**) revenue. .... When it was time to **add** **capacity**, wherein a segment of Flickr .... Even though the field of **data** **storage** has matured, let's look at a simple. .... Measure the characteristics of the **workload** for each piece of ... [www.scribd.com/.../The-Art-of-Capacity-Planning-by-OReilly-Media](#) - [Cached](#) - [Similar](#)

9. [Towards self-predicting systems: What if you could ask 'what-if?'](#)

Using these **data**, they can estimate the performance impact of **workload** or device ... explorations to optimize performance, especially in the area of **capacity** **planning**. ... (2001a) computes a good initial **configuration** of a **storage** system by ... utilization, a **simulation** module is needed to predict the **miss** rates ... [journals.cambridge.org/production/action/geoGetFulltext?fulltextid...](#) - [Similar](#)  
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10. [\[PDF\] IBM E server pSeries Sizing and \*\*Capacity\*\* \*\*Planning\*\*](#)

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A review of the various **data** gathering tools used for **capacity** **planning** .... **Workload**. **Configuration**. Inter-related. Transactions. Amount of **Data**. Number of users ..... **hit** **miss**. 8 cycles. Arb/**Add**. Memory. 2 for UP. **Data** Xfr. 3 cycles ..... mean that you can easily **add** **storage** on demand. Capitalizing on IBM ... [www.redbooks.ibm.com/redbooks/pdfs/sg247071.pdf](#) - [Similar](#)  
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#### 1. [SPECweb2009 Benchmark Design Document](#)

Mar 25, 2009 ... 2.8 **Storage**. 3.0 **Performance** Metrics. 3.1 Primary Metrics ... Rather than offering a single **benchmark workload** that attempts to approximate the ... Temperature: Temperature **data** is collected during all **iterations** for later ... during this interval will be recorded in the **final benchmark** results. ...

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#### 2. [PDF] [Hippodrome: running circles around storage administration](#)

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**performance** requirements of the **workload**. **Storage** system configuration is naturally an **iterative** ... the **final** system **design** in a small number of **iterations**. ... that Hippodrome can use an **initial performance** estimate .... The **data** for the entire PostMark **benchmark** has been sized to fit within a ...

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#### 3. [PS] [Workload Requirements for a Very High-Capacity Proxy Cache Design](#)

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culminated in our **initial** innovation in proxy. caching—FastCache v2.1. Using established web ... **performance** of proxy cache persistent **storage** by at ... to the **iterative** prototyping. and benchmarking performed with our new **workload**. **Benchmark** Development. ... proved invaluable during our **iterative** prototyping and ...

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#### 4. [PPT] [Workload Benchmarks that Scale in Multiple Dimensions](#)

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**Performance** = Work/Time. Work is usually undefined in **benchmarks**, ... Example of **Workload** Dimension: **Data** Parallelism (estimated) .... method uses an **iterative** solver and recursively subdivides all surfaces. ... Read problem geometry from nonvolatile **storage**. Create **initial** subdivision into large rectangles. ...  
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## 5. [\[PDF\] Including the Human Factor in Dependability Benchmarks](#)

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for the **final** dependability scores. We must also confront ..... comparison **benchmarks** or **iterative** benchmarking of the ... observing live human operators in an **initial benchmark** ... The **performance workload** consists of a realistic simula- ... For example, we will inject **storage** system failures (corrupt **data**, failed ...  
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## 6. [2 Designing and Developing for Performance](#)

Carefully consider **performance** issues during the **initial design** phase, ... Scalability is a system's ability to process more **workload**, .... Configuring the **initial** system architecture is a largely **iterative** process. .... for **storage** of aggregates and pre-joined **data** by clustering and materialized view functions. ...  
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## 7. [Improving Server Performance on Transaction Processing Workloads ...](#)

tions that reduce the energy (**Iterative** Improvement – II) ver- sus the SA algorithm. ... **initial data** placement before the **workload** is executed. 3.1. Solution State ... The nodes in the graph represent the **data storage** and compu- .... is similar to the schema specified by the **benchmark** and the ...  
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## 8. [\[PDF\] VMmark: A Scalable Benchmark for Virtualized Systems](#)

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Sep 25, 2006 ... occasionally conflict with the **design** goals of VMmark. ... **Benchmark** as a measure of Java **workload performance**. ... A single long **iteration** of the **benchmark** was run, ... CPU and **storage**. In many cases, database systems also face strict .... **Initial** VMmark results were generated to verify that a ...  
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### 9. A System Engineering Methodology for Predicting of Very Large Real ...

functional **benchmark** testing. System modeling and simulation **data storage** of 488 gigabytes (including all indices, ... engineering analysis, educated judgement, and **iterative** ... work used to balance **performance**, **design**, and costs. .... necessary to meet the required **workload** for the system under development. ...

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### 10. [PPT] Improving Server **Performance** on Transaction Processing **Workloads** ...

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Researchers have noticed that **performance** is hindered by **data** accesses ... on Transaction Processing **Workloads** by Enhanced **Data** Placement. 12. **Benchmark** ... annealing to generate an **initial data** layout based on the expected usage of the **data** ... We have to keep in mind that since this is an **iterative** process, ...

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